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		Application Number	10/776,397
		Filing Date	February 10, 2004
		First Named Inventor	Sohrab Kianian et al.
		Art Unit	2822
		Examiner Name	Toniae M. Thomas
Total Number of Pages in This Submission	/15	Attorney Docket Number	351913-992011 (2102397-992011)

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Firm Or Individual name	DLA Piper Rudnick Gray Cary US LLP
Signature	
Date	September 30, 2005

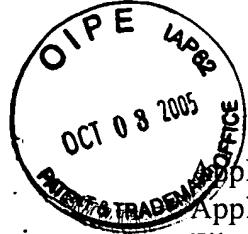
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Typed or printed name	Kathleen LaBrie		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/776,397
Applicant : Sohrab Kianian et al.
Filed : February 10, 2004
TC/A.U. : 2822
Examiner : Toniae M. Thomas
Title : SELF ALIGNED METHOD OF FORMING A SEMICONDUCTOR MEMORY ARRAY OF FLOATING GATE MEMORY CELLS WITH BURIED BIT-LINE AND VERTICAL WORD LINE TRANSISTOR

Docket No. : 2102397-992011
Customer No. : 26379

CERTIFICATE OF MAILING

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DLA PIPER RUDNICK GRAY CARY Date: 09/30/05

By: Kathleen LaBrie

Kathleen LaBrie

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION OF JUNE 30, 2005

Sir:

In response to the Office Action of June 30, 2005, please amend the above identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 10 of this paper.

10/04/2005 LWONDIM1 00000011 071896 10776397

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